

Gate Driver Unit 4DUD51016xFN1 for 3-Level IGBT

■Overview

- 4DUD51016xFN1 is a quad channel gate driver designed for Fuji Electric's
- IGBT power module $4MBI600VC{-}120{-}50$.
- This gate driver unit contains a built-in isolated DC/DC converter and gate drive circuit.
- It is ready to use by mounting it on the IGBT power module.

Features

- ·ldeal for drive of 3-Level inverter
- ·Ideal for drive of IGBT Power module 4MBI600VC-120-50 (Fuji Electric)
- •Gate voltage : +15V/-10V
- ·Gate resistor (T1,T4) : +1.5 Ω /-0.56 Ω (TYP)
- ·Gate resistor (T2,T3): $+1.2 \Omega / -0.56 \Omega (TYP)$
- •Short circuit detection voltage : 10V(TYP)
- $\cdot\,\text{ALL-IN-ONE}$ (Built-in isolated DC / DC converter and gate drive circuit)
- ·Low parasitic capacitance (12pF(TYP)) ; highly resistant to common-mode noise.
- •Fast response : About 130nsec(typ)
- •The isolation for primary-secondary signal used fast response isolator.
- ·Dielectric withstand voltage : AC5000V
- Insulation distance (clearance / creepage) : 14mm/14mm
- \cdot DC/DC converter input voltage : 13 \sim 28V
- •Power supply for gate driver input voltage : 13~28V
- ·Signal input voltage : $3.3V \sim 15V$ or 15V
- •Overload protection (DC/DC converter)
- ·Overheat protection (DC/DC converter)
- •Desaturation protection (Gate drive circuit / T1, T4)
- •Soft turn-off function (Gate drive circuit / T1, T4)
- •Fault signal output function (Gate drive circuit)
- ·Under-voltage lockout(UVLO) (Gate drive circuit)
- ·Active clamp protection function (Gate drive circuit)
- $\cdot Safety \ standards \\ \vdots \\ UL508 (file \ no. E243511) \ (DC/DC \ converter \ only) \\$
- ·UL1741, UL508, IEC62109-1, EN50178 compliant (Reinforced isolation according to IEC 60664-1)

Application

Industrial inverter, power conditioner, etc \cdots

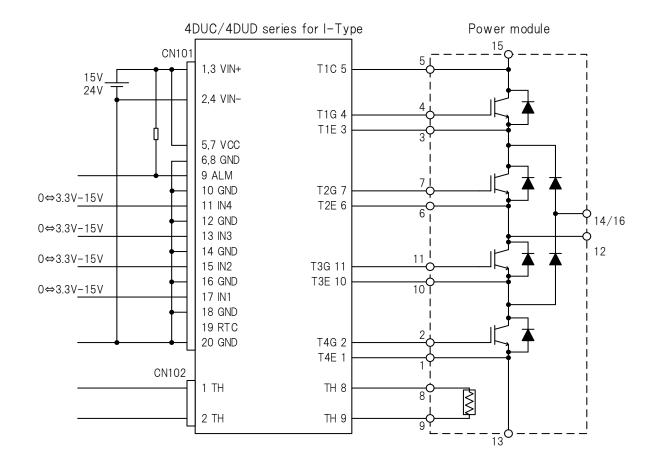
■Module information

Part number	Signal input voltage	Active clamp	Status
4DUD51016CFN1	3.3 to 15V	Yes	Active
4DUD51016DFN1	15V	Yes	Sample





■Circuit Image



■Pin Connection

CN101 : RA-H201SD (JST), 71922-120LF (FCI) or PS-20PE-D4LT2-M1E (JAE)

-					
Pin No.	Name	Function	Pin No.	Name	Function
1	VIN(+)	Power supply for DC/DC converter(+)	2	VIN(-)	Power supply for DC/DC converter(-)
3	VIN(+)	Power supply for DC/DC converter(+)	4	VIN(-)	Power supply for DC/DC converter(-)
5	VCC	Power supply for drive circuit	6	GND	Ground for drive circuit
7	VCC	Power supply for drive circuit	8	GND	Ground for drive circuit
9	ALM	Alarm signal output	10	GND	Ground for drive circuit
11	IN4	Control input 4	12	GND	Ground for drive circuit
13	IN3	Control input 3	14	GND	Ground for drive circuit
15	IN2	Control input 2	16	GND	Ground for drive circuit
17	IN1	Control input 1	18	GND	Ground for drive circuit
19	RTC	Recovery time of protection circuit control	20	GND	Ground for drive circuit
			D D D D D D D D D D		

%Recommend receptacle : RA-S201T (JST), 71600-020LF(FCI) or PS-20SM-D4P1-1*(JAE)

CN102 : S2B-XH-A For thermistor %Recommend receptacle : XHP-2 (JST)

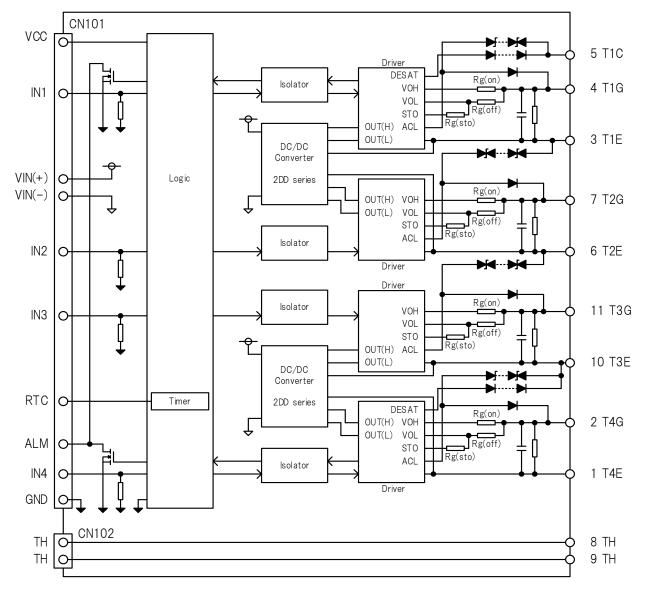
Connection on the power module

Pin No.	Name	Function	Pin No.	Name	Function
1	T4E	T4 Emitter connection	6	T2E	T2 Emitter connection
2	T4G	T4 Gate connection	7	T2G	T2 Gate connection
3	T1E	T1 Emitter connection	8	TH	For thermistor
4	T1G	T1 Gate connection	9	TH	For thermistor
5	T1C	T1 Collector connection	10	T3E	T3 Emitter connection
			11	T3G	T3 Gate connection



4DUD51016xFN1 Datasheet

■Internal Block Diagram



■I/O Condition Table

No. Status												Output		Ĩ
INU.	Status	OUT(H)	V_{CC}	T1C	T4C	IN1	IN2	IN3	IN4	ALM	T1G	T2G	T3G	T4G
1	V _{OUT} UVLO	UVLO	0	Х	Х	Х	Х	Х	Х	L	L	L	L	L
2	G-E short	0	0	Х	Х	Х	Х	Х	Х	Hi-Z	SD	SD	SD	SD
3	VccUVLO	0	UVLO	Х	Х	Х	Х	Х	Х	Hi-Z	L	L	L	L
4		0	0	L	L	L	Х	Х	Х	Hi-Z	L	Х	Х	Х
5		0	0	L	L	Н	Х	Х	Х	Hi-Z	Н	Х	Х	Х
6		0	0	L	L	Х	L	Х	Х	Hi-Z	Х	L	Х	Х
7	Normal	0	0	L	L	Х	Τ	Х	Х	Hi-Z	Х	Н	Х	Х
8	operation	0	0	L	L	Х	Х	L	Х	Hi-Z	Х	Х	L	Х
9		0	0	L	L	Х	Х	Н	Х	Hi-Z	Х	Х	Н	Х
10		0	0	L	L	Х	Х	Х	L	Hi-Z	Х	Х	Х	L
11		0	0	L	L	Х	Х	Х	Н	Hi-Z	Х	Х	Х	Н
12	Short circuit detection	0	0	Hi-Z	L	L	Х	Х	Х	Hi-Z	L	Х	Х	Х
13	(T1)	0	0	Hi-Z	L	Н	Х	Х	Х	L	L	Х	Х	Х
14	Short circuit detection	0	0	L	Hi–Z	Х	Х	Х	L	Hi–Z	Х	Х	Х	L
15	(T4)	0	0	L	Hi-Z	Х	Х	Х	Н	L	Х	Х	Х	L
	G-E short :	Gate-	Emitte	r short					O : 0	UT(H)	UVLO	> UVL	0, X :	Don't d
	SD: Shut down (Gate-Emitter short)													



■Absolute Maximum Ratings

lte	em		Symbol	Min	Max	Unit	Conditions · Note
Input voltage for DC/	DC co	nverter	V _{IN}	-0.3	28	Vdc	Between VIN(+) to VIN(-)
Input voltage for Gate	e drive	r	V _{CC}	-0.3	28	Vdc	Between VCC to GND
Input-side s	ignal v	oltage	$V_{\rm SG}$	-0.3	V _{cc} +0.3 or 18 *	V	IN1, IN2, IN3, IN4 *Whichever is less
			V_{RTC}	-0.3	5	V	RTC
Maximum gate curren	ıt		I _{GPEAK}	-	43	А	Excluding gate resistor
Switching frequency	Avera	ge	F _{sw}	-	7.5	kHz	Test load : 1.2Ω/232nF
Switching frequency	Peak		I SW	-	15	kHz	
Short circuit detectio	n pin v	oltage	V_{SD}	0	1200	V	
Alarm signal output p	in maxi	mum voltage	V_{ALM}	-	V _{CC} +0.3 or 28 *	V	ALM1,2 *Whichever is less
Input-side signal max	imum c	urrent	I _{ALM}	_	5	mA	ALM
Operating temperature	*00000	V _{IN} =13.5-18V	Τ _{ΟΡ}	-40	85	°C	See the derating ourse
Operating temperature range V _{IN} =18-26.4V		Τ _{ΟΡ}	-40	75	°C	See the derating curve	
Operating humidity			RH _{OP}	20	95	%RH	No condensation
Storage temperature	range		T _{STG}	-40	90	°C	
Storage humidity			RH _{STG}	5	95	%RH	No condensation

Recommended Operating Conditions

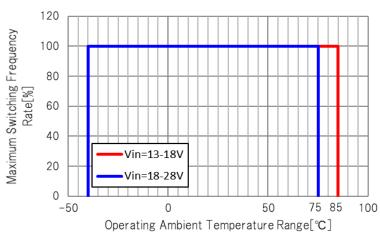
ltem	Symbol	Min	Max	Unit	Conditions.Note
Input voltage range for DC/DC converter	V _{IN}	13.5	26.4	Vdc	
Input voltage range for gate driver	V _{CC}	13.5	26.4	Vdc	
Driver circuit number	Ν	-	4	-	
Maximum gate charge	Q _G	-	5700	nC	
4DUx51016Cxxx					•
Logic high level input voltage	V_{SGH}	3.3	Vcc+0.3 or 16 *	V	IN1, IN2, IN3, IN4 *Whichever is less
Logic low level input voltage	V_{SGL}	-0.3	0.5	V	IN1, IN2, IN3, IN4
Source current of control signal	I _{SG}	3.2	-	mA	IN1, IN2, IN3, IN4 VSG=15V
4DUx51016Dxxx					•
Logic high level input voltage	V_{SGH}	13	Vcc+0.3 or 16 *	V	IN1, IN2, IN3, IN4 *Whichever is less
Logic low level input voltage	V _{SGL}	-0.3	0.5	V	IN1, IN2, IN3, IN4
Source current of control signal	I _{SG}	3.3	-	mA	IN1, IN2, IN3, IN4 VSG=15V
4DUD51016xxxx	•				•
DC-link voltage	V _{DCLINK}	-	750	V	





■Ambient Temperature Derating Curve

Reduce the switching frequency according to the following temperature derating table. Switching Frequency 100% = 7.5 kHz



■Electrical Specification (Vin=Vcc=15V.Ta=25°C, Unless otherwise specified)

	ltem	Symbol	Min	Тур	Max	Unit	Conditions · Note
DC/DC conver	ter	•					
Start-up voltag	ge	V_{START}	-	11.5	12.5	V	
Input current		I _{IN}	-	0.55	-	А	Fsw=7.5kHz, Test load : 1.2Ω/232nF
Standby power		P _{STBY}	_	2.5	_	W	No load
Logic inputs fo	r 4DUx51016Cxx	X					
Logic high leve	l input voltage	V_{SGH}	_	2.1	2.3	V	IN1, IN2, IN3, IN4 / Guaranteed by design
Logic low level	input voltage	V_{SGL}	1.0	1.2	-	V	IN1, IN2, IN3, IN4 $/$ Guaranteed by design
Logic pull-dow	n resistance	R_{SGD}	_	4700	-	Ω	IN1, IN2, IN3, IN4
Logic inputs fo	r 4DUx51016Dxx	X					
Logic high level input voltage		V_{SGH}	_	10.4	11.4	V	IN1, IN2, IN3, IN4 / Guaranteed by design
Logic low level	input voltage	V_{SGL}	4.9	5.9	-	V	IN1, IN2, IN3, IN4 / Guaranteed by design
Logic pull-dow	n resistance	R_{SGD}	-	4500	-	Ω	IN1, IN2, IN3, IN4
Gate driver out	put						
Output pin volta	age(High)	V _{OUTH}	14	15	16	V	No load
Output pin volta	age(Low)	V _{OUTL}	-11	-10	-9	V	No load
	Т1, Т4	Rg(ON)	_	1.5	Ι	Ω	
Gate resistor	11, 14	Rg(OFF)	_	0.56	Ι	32	
T2, T3		Rg(ON)	_	1.2	Ι	Ω	
12, 13		Rg(OFF)	_	0.56	Ι	32	
Auxiliary gate	Τ1, Τ4	Cge	-	100	-	nF	
capacitor T2, T3		Cge	-	OPEN	-	nF	
Delay time	Turn ON time	t _{PON}	-	130	-	ns	
	Turn OFF time	t _{POFF}	-	130	-	ns	





Protection

ltem	Symbol	Min	Тур	Max	Unit	Conditions · Note
DC/DC converter						•
Overload protection	-	10.5	-	-	W	Auto recovery
Overheat protection	-	120	-	150	°C	Auto recovery, Internal temperature
Gate driver						
VCC UVLO OFF voltage	VUVLOVCCH	_	9.1	-	V	
VCC UVLO ON voltage	VUVLOVCCL	-	7.3	-	V	
Output voltage(H) UVLO OFF voltage	V_{UVLOOHH}	13.2	13.5	13.8	V	Guaranteed by design
Output voltage(H) UVLO ON voltage	$V_{\rm UVLOOHL}$	12.2	12.5	12.8	V	Guaranteed by design
Short circuit detection voltage	V_{SD}	_	10	-	V	
Short circuit detection filter time	t _{SHORTFIL}	_	4.5	_	us	Collector open
Alarm signal output L voltage	V_{ALML}	-	-	0.5	V	I _{ALM} =5mA
Alarm signal output time	t _{ALM}	-	0.2	-	us	
Restart time	t _{restart}	_	110	_	ms	
Soft turn-off resistance	R _{STO}	_	15	-	Ω	
Soft turn-off duration	t _{sto}	_	4	_	us	

■Insulation

ltem	Specification	Conditions · Note
Between Input-Output		ŀ
Dielectric withstand voltage	AC5000V	1min, Cutoff 2mA
Insulation resistance	$100M\Omega$ or more	DC500V
Partial discharge extinction voltage	1768Vpeak or more	According to EN50178/IEC 60270
Common-mode transient immunity (CMTI)	70kV/us	
Minimum clearance distances	14mm	
Minimum creepage distances	14mm	
Between Output-Output		
Minimum clearance distances	8mm	
Minimum creepage distances	8mm	
Between Thermistor-Output		·
Minimum clearance distances	8mm	
Minimum creepage distances	8mm	





■Pin Function

·VIN(+), VIN(-) (Power supply pin for DC/DC converter)

- ·VCC(Power supply pin for drive circuit)
- •GND(Ground pin for drive circuit)

·IN1, IN2, IN3, IN4(Control input pin)

The IN1, IN2, IN3 and IN4 pin is a pin used to determine output logic.

IN1	IN2	IN3	IN4	T1G	T2G	T3G	T4G
L	Х	Х	Х	L	Х	Х	Х
Н	Х	Х	Х	Н	Х	Х	Х
Х	L	Х	Х	Х	L	Х	Х
Х	Н	Х	Х	Х	Н	Х	Х
Х	Х	L	Х	Х	Х	L	Х
Х	Х	Н	Х	Х	Х	Н	Х
Х	Х	Х	L	Х	Х	Х	L
X	Х	Х	Н	Х	Х	Х	Н

•RTC(Recovery time of protection circuit control pin)

When abnormality occurs (UVLO, short circuit detected), this pin is used to adjust the recovery time.

·ALM(Alarm signal output pin)

When abnormality occurs (UVLO, short circuit detected), This pin outputs an alarm signal. (Open drain)

Status	ALM
While in normal operation	Hi-Z
UVLO, When detecting short circuit	L



Description

1. Undervoltage Lockout (UVLO) function

The control circuit incorporates the undervoltage lockout (UVLO) function both on the VCC and the OUT(H) sides. When the OUT(H) voltage drops to the UVLO ON voltage, the Output pin and the ALM pin both will output the "L" signal. When the VCC voltage drops to the UVLO ON voltage, the Output pin will output the "L" signal. When the VCC or the OUT(H) voltage rises to the UVLO OFF voltage, these pins will be reset.

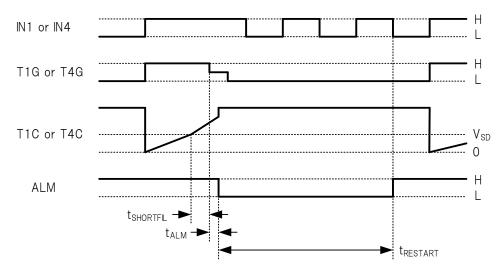
2. Short circuit protection function, Soft turn-off function (T1, T4)

When the collector pin voltage exceeds VSD, the short circuit protection function will be activated.

When the short circuit protection function is activated, the Output pin voltage will be set to the "L" level, and then the ALM pin voltage to the "L" level.

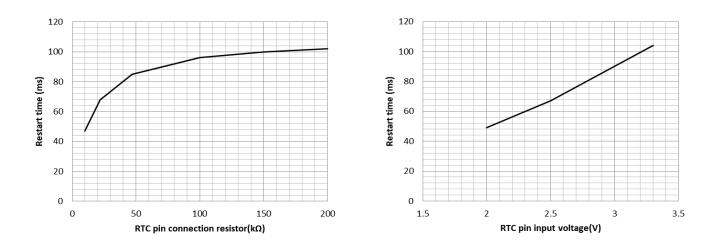
Also, soft turn-off function works to reduce collector-emitter voltage surge due to short circuit current.

Short-circuit protection is canceled automatically after an abnormal condition restart time and when the input signal is "L" level.



Timing chart of short circuit protection function

The restart time can be adjusted within the following range by the resistance or voltage connected to the RTC pin.







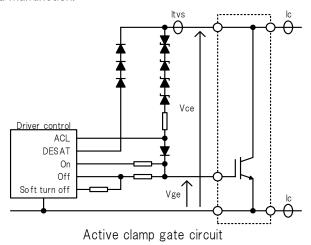
Description

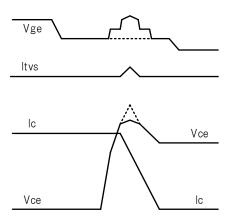
3. Active clamp function

The active clamp function protects the IGBT from a sudden voltage surge between the collector and emitter that occurs when the IGBT is turned off. Depending on the DC-LINK voltage and surge voltage, the losses that occur in TVS diodes and IGBTs increase. If a high surge voltage is clamped continuously, stress will be applied to the driver and IGBT. Therefore, the main circuit conditions (DC-LINK voltage, parasitic inductance, dlc / dt) should be optimized and designed so that the active clamp function does not operate normally. Use the TVS diode with a surface temperature of 120 $^{\circ}$ C or less.

When the collector-gate voltage exceeds the breakdown voltage of the active clamp TVS diode, current flows from the collector to the gate. The current (Itvs) supplies the gate with charge, partially turning on the IGBT and clamping the collector voltage. TVS loss can be obtained by time integration of the current (Itvs) and collector-emitter voltage (Vce).

To strengthen the clamp, the gate may rise even during the period when no current is flowing through the TVS diode, but this is not a malfunction.





Timing chart of Active clamp function

■Reliability

ltem	Test condition and acceptance criterion
Exposure in high temperature	IEC60068-2-2:2007: Test: Bb, 90℃, 240H, ※
Exposure in low temperature	IEC60068-2-1:2007: Test: Ab, -40°C, 240H, 💥
Exposure in high temperature and high humidity	IEC60068-2-78:2012-10, 60℃, 90~95%RH, 240H, ※
Thermal shock	IEC60068-2-14:2009-01: Test: Na, -40°C/30min to 100°C/30min, 500cycles, ※
Low temperature operation	IEC60068-2-1:2007: Test: Ae, Input voltage: DC24V, Output current: Rated Load
	-40°C, 240H, 💥
High temperature operation	IEC60068-2-2:2007: Test: Be, Input voltage: DC24V, Output current: Rated Load
	85°C, 240H, 💥
high temperature	IEC60068-2-78:2012-10, Input voltage: DC24V, Output current: Rated Load
and high humidity operation	85°C, 85%RH, 240H, 💥
Vibration	IEC 60068-2-6:2007-12, Frequency range: 10 to 55Hz, Sweep rate: 1.0oct/min,
	Displacement amplitude: \pm 0.75mm, Test duration: 120min, Axis: X,Y and Z, $$ $$ $$ $$ $$ $$
	IEC 60068-2-6:2007-12, Frequency range: 5 to 200Hz, Sweep rate: 1.0oct/min,
	Cross-over frequency: 8.4Hz, , Displacement amplitude(5 to 8,4Hz): \pm 3.5mm,
	Acceleration amplitude(8.4 to 200Hz):1G, Test duration: 20sweeps, Axis: X, Y and Z, 💥
Impact	IEC 60068-2-27:2008-02, Pulse shape: Half-sine, Corresponding duration: 11ms,
	Peak acceleration: 50G, Number of shocks:3, Axis: \pm X, Y and Z, $$ $\!$ $\!$ $\!$ $\!$ $\!$ $\!$ $\!$ $\!$
	IEC 60068-2-27:2008-02, Pulse shape: Half-sine, Corresponding duration: 6ms,
	Peak acceleration: 15G, Number of shocks:100, Axis: \pm X, Y and Z, $$ $$ $$ $$ $$ $$

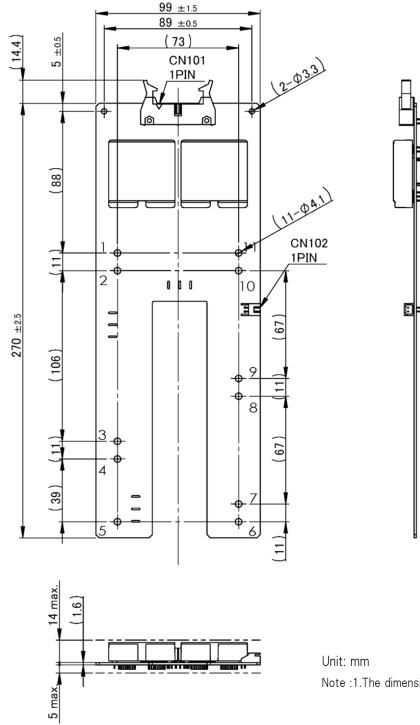
** After each test, exposure at room temperature and humidity condition for 24 hours.

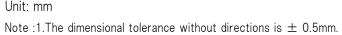
There shall be no abnormality on the electrical specification and appearance.



4DUD51016xFN1 Datasheet

■Outline Dimensional Drawing





■Product Weight

145g(TYP)

Storage Conditions

ltem	Min	Max	Unit	Conditions · Note
Storage temperature	-25	60	S	A packing state



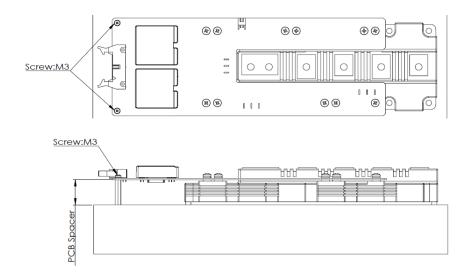


■Usage Cautions

Always mount fuse on the plus side of input for ensuring safety because the fuse is not built-in the product.
 Please select the fuse considering conditions such as steady current, inrush current, and ambient temperature.
 When using a fuse having large rated current or high capacity input electrolytic condenser, by combining another converter and input line and input electrolytic condenser, fuse may not blow off in the case of abnormality.
 Do not combine high voltage line and fuse.

- Make sure the rise/fall time of the input signal is 500ns or less.
 Also, keep input wiring as far as possible from noise sources.
 To prevent malfunction due to noise, we recommend the highest possible signal voltage within the recommended range.
- Please do not apply excessive stress to this product when attaching to IGBT power module.
 Please follow the device manufacturer's instructions on how to install the IGBT power module (type of screw used, material, tightening torque conditions, etc.).

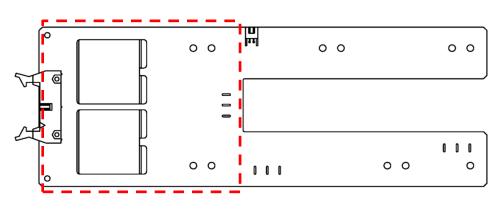
Also, if the product is exposed to vibration or shock, the PCB should be fixed with spacers as shown in the figure below.



• Handling of this product before IGBT installation

In order to avoid damage to mounted components due to warpage of the PC board,

the product should be gripped within the area indicated by the red dotted line in the figure below.



This product has DESAT protection for arm short circuit and load short circuit protection.
 However, even if this protection works, the IGBT may be damaged if abnormally high current occurs due to IGBT's characteristics variations or the load short-circuit mode during parallel operation.

To ensure safety, be sure to check the short-circuit current at the unit in which this product is integrated,

and evaluate whether it can protect under the condition that there is no damage to the IGBT.





Important Notice

- This information and product are subject to change without prior notice for the purpose of improvements, etc. Ensure that you are in possession of the most up-to-date information when using this product.
- The operation examples and circuit examples shown in this document are for reference purposes only, and TAMURA Corporation disclaims all responsibility for any violations of industrial property rights, intellectual property rights and any other rights owned by TAMURA Corporation or third parties that these may entail.
- The circuit examples and part constants listed in this document are provided as reference for the verification of characteristics. You are to perform design, verification, and judgment at your own responsibility, taking into account the various conditions.
- TAMURA has evaluated the efficiency and performance of this product in a usage environment determined by us. Depending on your usage environment or usage method, there is the possibility that this product will not perform sufficiently as shown in the specifications, or may malfunction.
 When applying this product to your devices or systems, please ensure that you conduct evaluations of their state when integrated with

When applying this product to your devices or systems, please ensure that you conduct evaluations of their state when integrated with this product. You are responsible for judging its applicability.

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systems or this product which are caused by your usage environment or usage method.

- TAMURA Corporation constantly strives to improve quality and reliability, but malfunction or failures are bound to occur with some probability in power products. To ensure that failures do not cause accidents resulting in injury or death, fire accidents, social damage, and so on, you are to thoroughly verify the safety of their designs in devices and/or systems, at your own responsibility.
- This product is intended for use in consumer electronics (electric home appliances, business equipment, Information equipment, communication terminal equipment, measuring devices, and so on.) If considering use of this product in equipment or devices that require high reliability (medical devices, transportation equipment, traffic signal control equipment, fire and crime prevention equipment, aeronautics and space devices, nuclear power control, fuel control, in-vehicle equipment, safety devices, and so on), please consult a TAMURA sales representative in advance. Do not use this product for such applications without written permission from TAMURA Corporation.
- This product is intended for use in environments where consumer electronics are commonly used. It is not designed for use in special environments such as listed below, and if such use is considered,
 - you are to perform thorough safety and reliability checks at your own responsibility.
 - Use in liquids such as water, oil, chemical solutions, or organic solvents, and use in locations where the product will be exposed to such liquids.
 - Use that involves exposure to direct sunlight, outdoor exposure, or dusty conditions.
 - Use in locations where corrosive gases such as salt air, C12, H2S, NH3, SO2, or NO2, are present.
 - · Use in environments with strong static electricity or electromagnetic radiation.
 - · Use that involves placing inflammable material next to the product.
 - Use of this product either sealed with a resin filling or coated with resin.
 - · Use of water or a water soluble detergent for flux cleaning.
 - Use in locations where condensation is liable to occur.
- This product is not designed to resist radiation.
- This product is not designed to be connected in series or parallel.
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