

Gate Driver Module 2CG010DBC12N

■ Overview

Gate driver 2CG010DBC12N is a dual channel gate driver designed for IGBT.

The high breakdown voltage and low parasitic capacitance make it suitable for gate drives such as IGBT.

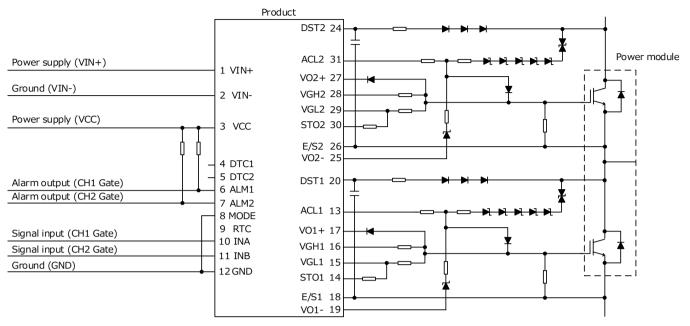
■ Features

- · Ideal for drive of IGBT
- Gate voltage : +15V/-15V
- · ALL-IN-ONE (Built-in isolated DC / DC converter and gate drive circuit)
- · Low parasitic capacitance (about 12pF); highly resistant to common-mode noise.
- Fast response : about 100nsec(typ)
- $\boldsymbol{\cdot}$ The isolation for primary-secondary signal used fast response isolator.
- · Input-to-Output dielectric withstand voltage: AC5000V
- · Output CH1-to-Ouput CH2 dielectric withstand voltage: AC4000V
- · Input-to-Output insulation distance: 14mm (clearance), 16mm(creepage) (As for Gate driver module PCB)
- · Output CH1-to-Output CH2 insulation distance : 7mm (clearance), 12mm(creepage)
- DC/DC converter input voltage :13~28V
- · Signal input voltage: 3.3V,5V
- Overload protection (DC/DC converter)
- Overheat protection (DC/DC converter)
- · Half bridge mode (Gate drive circuit)
- · Desaturation protection (Gate drive circuit)
- \cdot Soft turn-off function (Gate drive circuit)
- Fault signal output function (Gate drive circuit)
- · Active clamp function (Gate drive circuit)
- Under-voltage lockout(UVLO) (Gate drive circuit)
- · Insulating moistureproof coating
- · Safety standards: UL508(file no.E243511) (DC/DC converter only)
- $\cdot \ \text{Reinforced isolation according to IEC 60664-1 (IEC61800-5-1, IEC62477-1, IEC62109-1, etc.)}\\$
- · UL compliant (UL1741, UL508, etc.)

■ Application

Industrial inverter, power conditioner, etc ...

■Circuit Image



■ Pin Connection

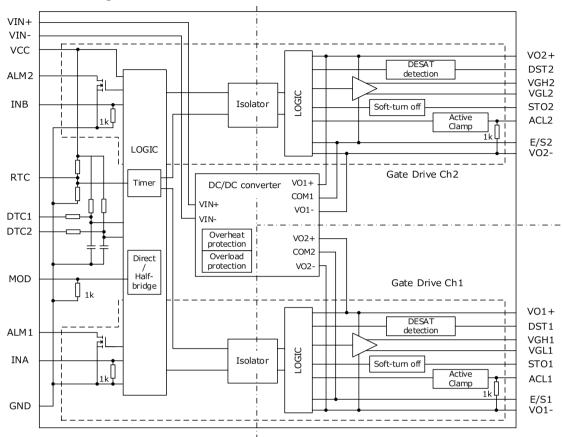
Input

Pin No.	Name	CH	Function					
1	VIN+	Common	Power supply for DC/DC converter(+)					
2	VIN-	Common	Power supply for DC/DC converter(—)					
3	VCC	-	Power supply for drive circuit					
4	DTC1	1	Dead time adjustment					
5	DTC2	2	Dead time adjustment					
6	ALM1	1	Alarm signal output					
7	ALM2	2	Alarm signal output					
8	MOD	-	Mode select					
9	RTC	-	Restart time of protection circuit control					
10	INA	1	Control input A					
11	INB	2	Control input B					
12	GND	-	Ground for drive circuit					

Output

Pin No.	Name	CH	Function				
13	ACL1	1	Active clamp pin				
14	STO1	1	Soft turn off pin				
15	VGL1	1	Gate OFF side pin				
16	VGH1	1	Gate ON side pin				
17	VO1+	1	DC/DC converter output pin				
18	E/S1	1	Emitter · source connection pin				
19	VO1-	1	DC/DC converter output pin				
20	DST1	1	Desaturation protection pin				
21	NONE	-	None				
22	NONE	-	None				
23	NONE	-	None				
24	DST2	2	Desaturation protection pin				
25	VO2-	2	DC/DC converter output pin				
26	E/S2	2	Emitter · source connection pin				
27	VO2+	2	DC/DC converter output pin				
28	VGH2	2	Gate ON side pin				
29	VGL2	2	Gate OFF side pin				
30	STO2	2	Soft turn off pin				
31	ACL2	2	Active clamp pin				

■ Internal Block Diagram



■I/O Condition Table

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No.	Status				Inj	put			Output(CH2)				Output(CH1)				
140.	Status	VO+	DST2	DST1	ACL2	ACL1	MOD	INB	INA	ALM2	VGH2	VGL2	STO2	ALM1	VGH1	VGL1	STO1
1	VO _X + UVLO	UVLO	Χ	Χ	Χ	Х	Χ	Х	Χ	L	Hi-Z	Hi-Z	┙	L	Hi-Z	Hi-Z	L
2	ES-VO _X - short	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	L	Hi-Z	SD	SD	L	Hi-Z	SD	SD
3	Normal	0	ı	L	ı	L	L	-	L	-	ı	-	ı	Hi-Z	Hi-Z	L	L
4	operation	0	1	L	ı	L	L	-	Ι	-	1	-	1	Hi-Z	Η	Hi-Z	Hi-Z
5	(Direct	0	L	-	L	-	L	L	ı	Hi-Z	Hi-Z	L	┙	ı	-	ı	-
6	Mode)	0	L	-	L	-	L	Н	ı	Hi-Z	Н	Hi-Z	Hi-Z	1	-	-	-
7	Normal	0	L	L	L	L	Н	L	Χ	Hi-Z	Hi-Z	L	L	Hi-Z	Hi-Z	L	L
8	operation (Half-bridge	0	L	L	L	L	Н	Н	L	Hi-Z	Н	Hi-Z	Hi-Z	Hi-Z	Hi-Z	L	L
9	Mode)	0	L	L	L	L	Н	Н	Н	Hi-Z	Hi-Z	L	L	Hi-Z	Н	Hi-Z	Hi-Z
10		0	1	Hi-Z	ı	L	L	-	L	-	1	-	1	Hi-Z	Hi-Z	┙	L
11	Desaturation	0	ı	Hi-Z	ı	L	L	-	Ι	-	ı	-	ı	L	Hi-Z	Hi-Z	L
12	protection1	0	1	Hi-Z	ı	L	Н	Н	L	-	1	-	1	Hi-Z	Hi-Z	┙	L
13		0	ı	Hi-Z	ı	L	Н	Н	Ι	-	ı	-	ı	L	Hi-Z	Hi-Z	L
14		0	Hi-Z	-	L	-	L	L	ı	Hi-Z	Hi-Z	L	┙	1	-	1	-
15	15 Desaturation 16 protection2	0	Hi-Z	-	L	-	L	Н	-	L	Hi-Z	Hi-Z	L	1	-	-	-
16		0	Hi-Z	-	L	-	Н	Н	Η	Hi-Z	Hi-Z	L	L	1	-	-	-
17		0	Hi-Z	-	L	-	Н	Н	L	L	Hi-Z	Hi-Z	L	1	-	-	-
18	Active	0	1	L	-	Н	L	-	L	-	-	-	-	Hi-Z	Hi-Z	Hi-Z	L
19	clamp	0	L	-	Н	-	L	L	-	Hi-Z	Hi-Z	Hi-Z	Ĺ	-	-	-	-

○ : Vox+ UVLO > UVLO, X : Don't care, SD : Shut down



■ Absolute Maximum Ratings

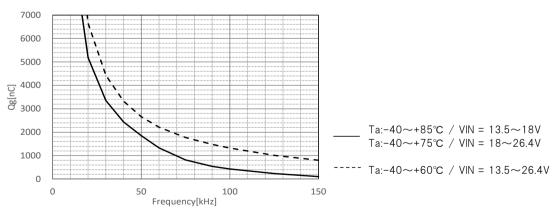
Item	Symbol	Min	Max	Unit	Conditions · Note	
Input voltage for DC/DC converter			-0.3	28	Vdc	Between VIN+ to VIN-
Input-side signal voltage	V_{CC} , V_{SG}	-0.3	5.5	٧	VCC, RTC, MOD, INA, INB	
input-side signal voltage	V _{ALM}	-0.3	28	٧	ALM1, ALM2	
Input-side signal maximur	I_{ALM}	-	5	mA	ALM1, ALM2	
DESAT pin input voltage	V_{DESAT}	-0.3	$V_{GH} + 0.3$	V		
Active clamp pin input volt	V_{CLAMP}	V_{GL} -0.3	16	V	Between ACLx to VOx- / Duty:2% or less	
Maximum gate current	I_{GPEAK}	-	43	Α		
DC/DC converter output pe	ower	P _{OUT}	-	3.3	W	Per output circuit
Switching frequency		F _{SW}	-	150	kHz	See the permissible frequency curve
Operating temperature range	V _{IN} =13.5-18V	T _{OP}	-40	85	$^{\circ}$	See the permissible frequency curve
V _{IN} =18-26.4V		T _{OP}	-40	75	$^{\circ}$	See the permissible frequency curve
Operating humidity	RH _{OP}	20	95	%RH	No condensation	
Storage temperature range	T _{STG}	-40	90	$^{\circ}$		
Storage humidity		RH_{STG}	5	95	%RH	No condensation

■ Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions · Note
Input voltage range for DC/DC converter	V_{IN}	13.5	26.4	Vdc	
Input-side signal voltage range	$V_{CC,}V_{SG}$	3	5.5	Vdc	
Driver circuit number	N	-	2	-	
Logic high level input voltage	V_{SGH}	V _{CC} x0.7	-	V	MOD, INA, INB
Logic low level input voltage	V_{SGL}	-	V _{CC} x0.3	V	MOD, INA, INB
Source current of control signal	I_{SG}	5	-	mA	MOD, INA, INB V _{SG} =5V
Maximum gate drive capability (150 kHz)	Q_{MAX}	-	100	nC	Gate current (ave) = 15mA Reference value
Maximum gate drive capability (30 kHz)	Q_{MAX}	-	3350	nC	Gate current (ave) = 100mA Reference value
Maximum gate charge amount	Q_{G}	-	7000	nC	
Minimum input pulse width	t_{INMSK}	60	-	ns	

■ Permissible frequency curve

Total gate charge (Qg) vs permissible frequency curve



If the active clamp is to be operated repeatedly, subtract 10% from the above curve.

4 / 6 TAMURA CORPORATION TMRDM0064EN



■ Electrical Specification (Vin=24V, Vcc=5V.Ta=25°C, Unless otherwise specified)

Item		Symbol	Min	Тур	Max	Unit	Conditions · Note
DC/DC conver	rter						
Start-up voltage		V_{START}	-	-	13	V	
Input current		I_{IN}	-	0.35	-	Α	Fsw=14.5kHz / Test load: 233nF
Standby power	er	P_{STBY}	-	1.2	-	W	No load
Logic inputs							•
Logic high level input voltage		V_{SGH}	V _{CC} x0.7	-	-	V	MOD, INA, INB / Guaranteed by design
Logic low level input voltage		V_{SGL}	-	-	V _{CC} x0.3	V	MOD, INA, INB / Guaranteed by design
Logic pull-down resistance		R_{SGD}	-	1000	-	Ω	MOD, INA, INB
Gate driver ou	utput	-					•
Gate ON side pir	n / output voltage(+)	V_{GH} , V_{O+}	14	15	16	V	No load
Gate OFF side pin / output voltage(-)		V _{GL} ,V _O -	-16	-15	-14	V	No load
Delay time	Turn ON time	t_{PON}	-	100	-	ns	
Delay time	Turn OFF time	t _{POFF}	-	100	-	ns	
Dead time		t_{DEAD}	-	4.1	-	us	Half bridge mode

■ Protection

Item	Symbol	Min	Тур	Max	Unit	Conditions · Note
DC/DC converter						•
Overload protection	-	8.8	-	-	W	Auto recovery
Overheat protection	-	120	-	150	$^{\circ}$	Auto recovery, Internal temperature
Gate driver						
Output voltage(H) UVLO OFF voltage	V_{UVLOGHH}	13.2	13.5	13.8	V	Guaranteed by design
Output voltage(H) UVLO ON voltage	V_{UVLOGHL}	12.2	12.5	12.8	V	Guaranteed by design
DESAT charge current	I_{DESAT}	200	240	280	uA	Guaranteed by design
DESAT detection voltage	V_{DESAT}	6.0	6.35	7.0	V	Guaranteed by design
DESAT detection filter time	t _{DSTFIL}	-	400	-	ns	DSTxpin open
DESAT detection time	t _{DSTOUT}	-	450	-	ns	DSTxpin open
Alarm signal output L voltage	V_{ALML}	-	-	0.5	V	I _{ALM} =5mA
Alarm signal output time	t _{ALM}	-	650	-	ns	
Restart time	t _{RESTART}	-	110	-	ms	
Soft turn off duration	t_{STO}	-	4	-	us	

5 / 6 TAMURA CORPORATION TMRDM0064EN



■ Insulation

Item	Specification	Conditions · Note		
Between Input-Output				
Dielectric withstand voltage	AC5000V	1min, Cutoff 2mA		
Insulation resistance	100M Ω or more	DC500V		
Partial discharge extinction volt.	1768Vpeak or more	According to EN50178/IEC 60270		
Common-mode transient immunity (CMTI)	70kV/us			
Minimum clearance distances	14mm			
Minimum creepage distances	16mm	As for Gate driver module PCB		
Between CH1-CH2				
Dielectric withstand voltage	AC4000V	1min, Cutoff 2mA		
Insulation resistance	100M Ω or more	DC500V		
Partial discharge extinction volt.	1700Vpeak or more	According to EN50178/IEC 60270		
Minimum clearance distances	7mm			
Minimum creepage distances	12mm	As for Gate driver module PCB		

■ Storage Conditions

Item	Min	Max	Unit	Conditions · Note
Storage temperature	-25	60	$^{\circ}$	A packing state

^{*}If you want to use past the long period there is a concern that the solder non-wetting by terminal oxidation to occur.

Therefore, please use from taking enough tests.

■ Recommended Soldering Condition

• Flow soldering condition : 255 ± 3 °C Less than 3sec

Temperature of preheating $110^{\circ}\text{C} \sim 130^{\circ}\text{C}$ End temperature of preheating $110^{\circ}\text{C} \pm 10^{\circ}\text{C}$

Soldering condition of hand work
 : 360℃(MAX) Less than 3sec

■ Usage Cautions

- Always mount fuse on the plus side of input for ensuring safety because the fuse is not built-in the product.
 Please select the fuse considering conditions such as steady current, inrush current, and ambient temperature.
 When using a fuse having large rated current or high capacity input electrolytic condenser, by combining another converter and input line and input electrolytic condenser, fuse may not blow off in the case of abnormality.
 Do not combine high voltage line and fuse.
- Make sure the rise/fall time of the input signal is 500ns or less.
- This product has DESAT protection for arm short circuit and load short circuit protection.

However, even if this protection works, the IGBT may be damaged if abnormally high current occurs due to IGBT's characteristics variations or the load short-circuit mode during parallel operation.

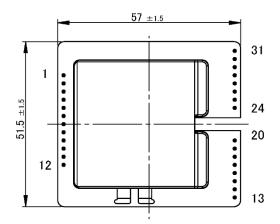
To ensure safety, be sure to check the short-circuit current at the unit in which this product is integrated, and evaluate whether it can protect under the condition that there is no damage to the IGBT.

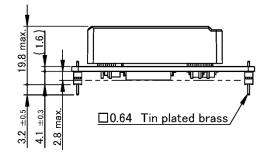
The coating material is applied to the product, so it may appear to be partially whitened.
 This does not affect the characteristics of the product.

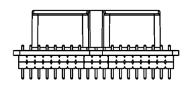
6 / 6 TAMURA CORPORATION TMRDM0064EN

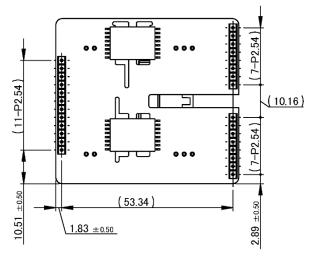


■ Outline Dimensional Drawing









Unit: mm

Note: 1. The dimensional tolerance without directions is \pm 0.5mm.

■ Product Weight

45.0g(typ)



■ Important Notice

- This information and product are subject to change without prior notice for the purpose of improvements, etc.
 Ensure that you are in possession of the most up-to-date information when using this product.
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 - Use that involves exposure to direct sunlight, outdoor exposure, or dusty conditions.
 - Use in locations where corrosive gases such as salt air, C12, H2S, NH3, SO2, or NO2, are present.
 - $\boldsymbol{\cdot}$ Use in environments with strong static electricity or electromagnetic radiation.
 - $\boldsymbol{\cdot}$ Use that involves placing inflammable material next to the product.
 - Use of this product either sealed with a resin filling or coated with resin.
 - · Use of water or a water soluble detergent for flux cleaning.
 - · Use in locations where condensation is liable to occur.
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